

Semiconductor Integrated Circuits Layoutdesign Act, 2000

Section 2 - Definitions.—In this Act, unless the context otherwise requires,—

2000.

(2) It extends to the whole of India.

(3) It shall come into force on such date¹ as the Central Government may, by notification in the Official Gazette, appoint; and different dates may be appointed for different provisions of this Act, and any reference in any such provision to the commencement of this Act shall be construed as a reference to Section 2 - the coming into force of that provision (a) Appellate Board means the Appellate Board established under section 32;

(b) assignment means an assignment in writing by act of the parties concerned;

(c) Bench means a Bench of the Appellate Board;

(d) Chairperson means the Chairperson of the Appellate Board;

(e) commercial exploitation, in relation to Semiconductor Integrated Circuits Layout-Design, means to sell, lease, offer or exhibit for sale or otherwise distribute such semiconductor integrated circuit for any commercial purpose;

(f) convention country means a country notified as such under section 93;

(g) Judicial Member means a Member of the Appellate Board appointed as such under this Act, and includes the Chairperson or such Vice-Chairperson who possesses any of the qualifications specified in sub-section (3) of section 34;

1. 1st May, 2004 (Ss. 3 and 5), vide notification No. S.O. 278(E), dt. 12th February, 2004, see Gazette of India, Extraordinary,