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Semiconductor Integrated Circuits Layout Design Act, 2000

Section 1 - Short title, extent and commencement.—

THE SEMICONDUCTOR INTEGRATED CIRCUITS LAYOUT-DESIGN ACT, 2000 ACT NO. 37 OF 2000 [4th September, 2000.] An Act to provide for the protection of semiconductor integrated circuits layout-designs and for matters connected therewith or incidental thereto. WHEREAS the Final Act embodying the results of the Uruguay Round of Multilateral Trade Negotiations done at Marrakesh on the 15th day of April, 1994 provides for establishment of the World Trade Organisation; AND WHEREAS the Agreement on Trade Related Aspects of Intellectual Property Rights is part of the said Final Act; AND WHEREAS the Government of India, having ratified the said Final Act, should, inter alia, make provisions for giving effect to section 6 in Part II of the Agreement on Trade Related Aspects of Intellectual Property Rights relating to Layout-Design (Topographies) of Integrated Circuits. BE it enacted by Parliament in the Fifty-first Year of the Republic of India as follows: CHAPTER I Section 1 - PRELIMINARY 1) This Act may be called the Semiconductor Integrated Circuits Layout-Design Act,