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Semiconductor Integrated Circuits Layout-design Act, 2000

Section 58 - Penalty for Improperly Describing a Place of Business as Connected with the Semiconductor Integrated Circuits Layout-design Registry

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If any person uses on his place of business, or on any document issued by him, or otherwise, words which would reasonably lead to the belief that his place of business is, or is officially connected with, the Semiconductor Integrated Circuits Layout-Design Registry, he shall be punishable with imprisonment for a term which may extend to six months, or with fine, or with both.

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