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Semiconductor Integrated Circuits Layout-design Act, 2000

Section 56 - Penalty for Infringement of Layout-design

Any person who contravenes knowingly and wilfully any of the provisions of section 18 shall be punishable with imprisonment for a term which may extend to three years, or with fine which shall not be less than fifty-thousand rupees but which may extend to ten lakh rupees, or with both.
