

Semiconductor Integrated Circuits Layout-design Act, 2000

Section 5 - Registry

(1) For the purposes of this Act, there shall be established a Registry which shall be known as the Semiconductor Integrated Circuits Layout-Design Registry.

(2) The head office of the Semiconductor Integrated Circuits Layout-Design Registry shall be at such place as the Central Government may specify and for the purposes of facilitating the registration of layout-designs, there may be established, at such places as the Central Government may think fit, branch offices of the Semiconductor Integrated Circuits Layout-Design Registry.

(3) The Central Government may, by notification in the Official Gazette, define the territorial limits within which an office of the Semiconductor Integrated Circuits Layout-Design Registry may exercise its functions.

(4) There shall be seal of the Semiconductor Integrated Circuits Layout-Design Registry.
